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SPECIFICATION FOR SMT OSCILLATOR
MtronPTI P/N: M2002S692

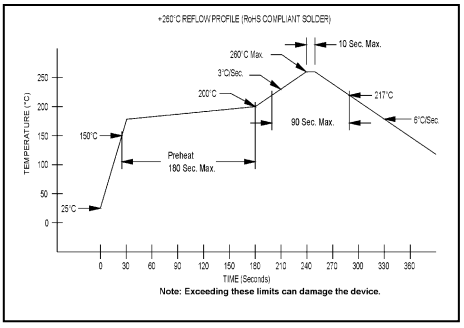
I. GENERAL & ELECTRICAL REQUIREMENTS:

1. FREQUENCY OF OPERATION: 65.536000 MHz
2. FREQUENCY STABILITY @ +25°C: ±100 ppm (Includes initial calibration, deviation over operating temperature, load, supply, shock, vibration and 10 year aging).
3. OPERATING TEMPERATURE RANGE: -40°C to +85°C
4. OPERATING VOLTAGE (Vdd): 3.3 V ± 10%
5. OPERATING CURRENT: 30 mA max.
6. OUTPUT TYPE: HCMOS/TTL Compatible.
7. SYMMETRY: 45/55% ref. to ½ Vdd.
8. RISE/FALL TIME: 4.0 nS max. ref. between 20% and 80% Vdd
9. OUTPUT LOGIC LEVELS: V_{OL} = 10% Vdd max. V_{OH} = 90% Vdd min.
10. PHASE JITTER: 5 ps RMS max. (12 kHz to 20 MHz)
11. OUTPUT LOAD: 15 pF max.
12. START-UP TIME: 10 msec. max.
13. TRISTATE FUNCTION (Pad 1): Logic "1" or "floating", clock signal output
    Logic "0", output disables to high impedance state

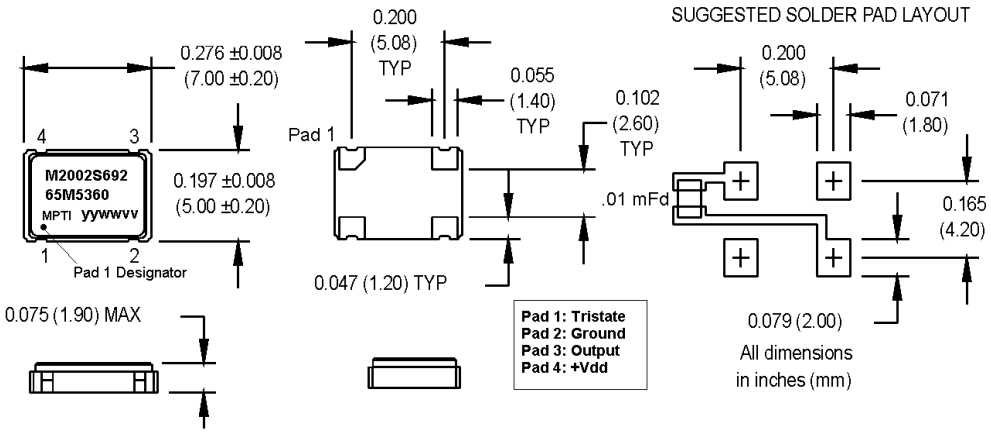
II. ENVIRONMENTAL & MECHANICAL REQUIREMENTS:

1. SHOCK: MIL-STD-202, Method 213, Condition C.
2. VIBRATION: MIL-STD-202, Methods 201 & 204.
3. HERMETICITY: 1 X 10^{-8} atm cc/sec min.
4. STORAGE TEMPERATURE: -55°C to +125°C
5. SOLDERABILITY: Per EIAJ-STD-002
6. MAXIMUM SOLDERING CONDITIONS: See Figure 1.
7. PACKAGE: 4 - pad leadless ceramic package. RoHS compliant.

Figure 1



III. DIMENSIONS:



IV. DATA SHEET REVISION TABLE:

Date	Rev.	PCN	Details of Revision
3/12/07	0	N/A	Original release.
3/16/07	A	N/A	Changed Symmetry to 45/55% and added 10 yr.aging to the Stability spec. Supersedes 3/12/07 release.