



SPECIFICATION FOR 1.8V CMOS SMT TCXO

MtronPTI P/N M6302S036

Electrical Specifications:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Frequency of Operation	F_O		25.000000		MHz	
Frequency Tolerance		-1.0		+1.0	ppm	@ +25°C
Frequency Stability						
vs. Temperature	$\Delta F/F$			4.6	ppm	(Max-Min)/2
vs. Aging		-3		+3	ppm	1 st year
		-1		+1	ppm	Per year thereafter.
RF Output						
Output Type		HCMOS Compatible				
Output Load				15	pF	
Logic "1" Level	V_{OH}	90% V_{DD}				
Logic "0" Level	V_{OL}			10% V_{DD}		
Symmetry (duty cycle)	T_{DC}	45		55	%	
Rise/Fall Time	T_R/T_F			6	nS	@50% V_{DD}
Enable/Disable Function		80% V_{DD}			V	Pad 1: Output Enabled
				0.35	V	Pad 1: Output Disabled
Phase Noise			-77		dBc/Hz	@10Hz offset
			-109		dBc/Hz	@100Hz offset
			-129		dBc/Hz	@1kHz offset
			-137		dBc/Hz	@10kHz offset
			-141		dBc/Hz	@100Hz offset
Supply Voltage & Power Consumption						
Operating Voltage	V_{CC}	1.71	1.8	1.89	V	
Operating Current	I_{CC}			90	mA	

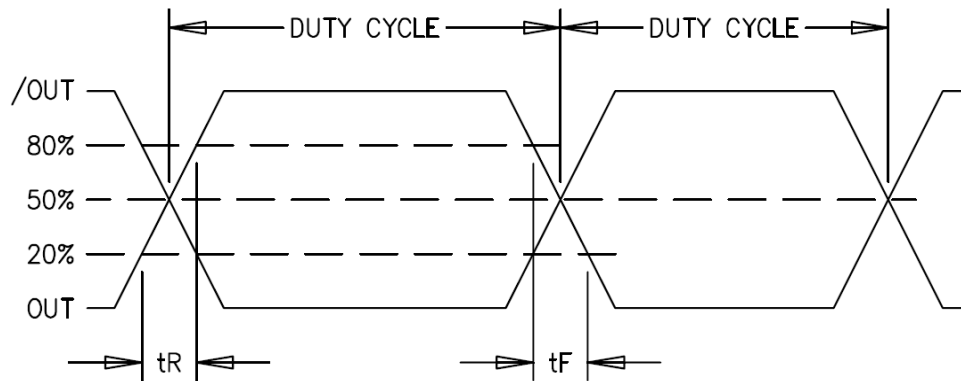
Environmental Conditions:

Parameter	Symbol	Min.	Typ.	Max.	Units	Conditions
Operating Temperature	T _A	-55		+125	°C	
Storage Temperature	T _S	-55		+125	°C	
Shock	Per MIL-STD-202, Method 213, Condition C (100 g's, 6 ms duration, ½ sinewave)					
Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)					
Thermal Shock	Per MIL-STD-883, Method 1011, Condition A					
Thermal Cycle	Per MIL-STD-883, Method 1010, Condition B					
Hermeticity	Per MIL-STD-202, Method 112 (1 x 10 ⁻⁸ atm cc/s of helium)					
Moisture Sensitivity Level (MSL)	MSL 1					
Solderability	Per EIAJ-STD-002					
Max. Soldering Conditions	See Figure 1.					
Package Type	6-pad 5.0 X 7.0 X 1.9 mm leadless ceramic. RoHS compliant.					



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Output Waveform:



Soldering Conditions:

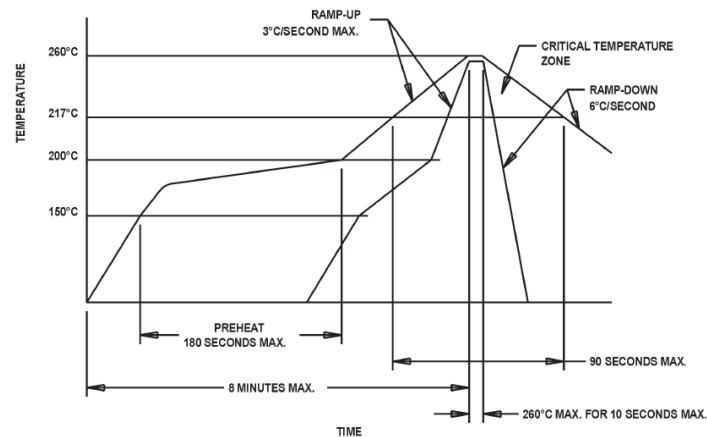


Figure 1



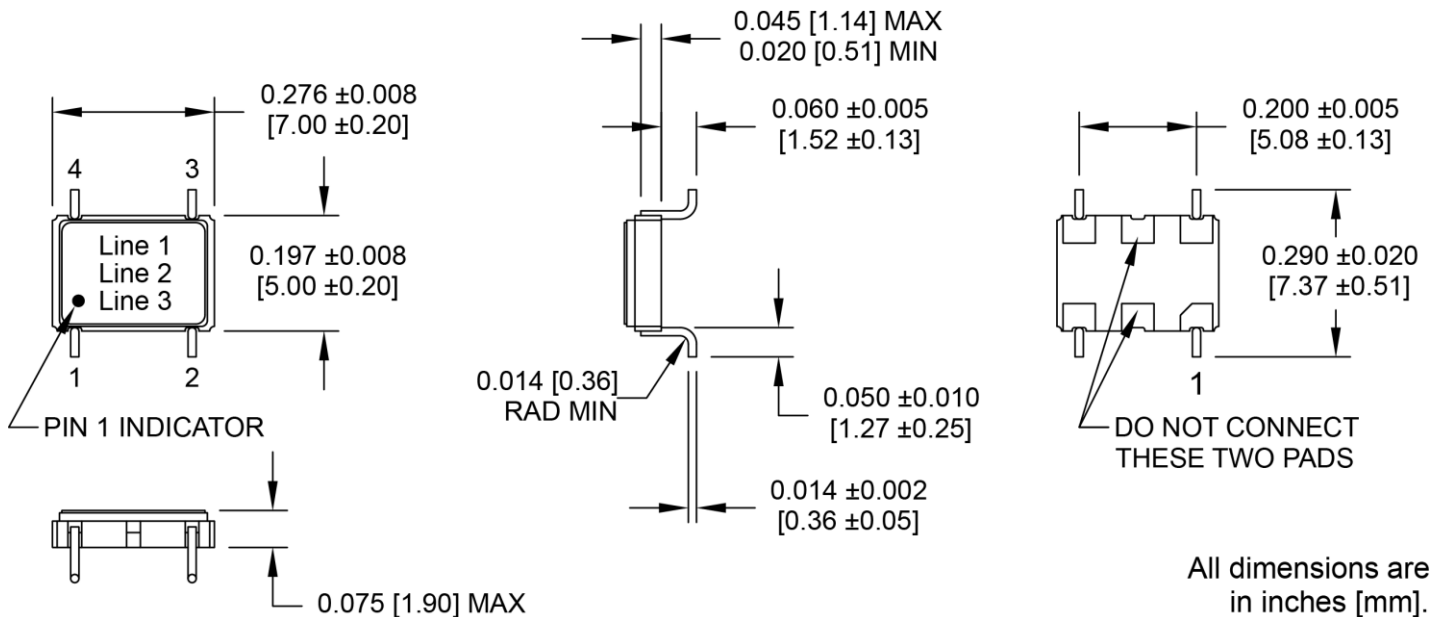
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Mechanical, Marking, and Pin Out Information:

Part Marking	
Line 1	M6302S036
Line 2	25.000M
Line 3	yyww

Legend	
yy	Year
ww	Work week

Pin	Function
1	Tri-state
2	Ground
3	Output
4	+V _{DD}



Datasheet Revision Table:

Date	Rev	Author	Details of Revision
01-22-20	C	BRR	Pad 1 output disabled changed from 20%VDD to 0.35V per PCN-10418
01-16-20	B	BRR	Customer added to header, outline updated, part marking updated.
11-19-19	A	BRR	Original release.